Amendments to the Specification

On page 4, line 20, please replace the paragraph with:

"In Figure ~~2~~3, the interrupt signals are received at 40 by CPU 12. The CPU then determines whether or not the interrupts are from a local device, such as 20, or from a device across the expansion bus such as 24. If there are interrupts from both the local devices and the expansion devices, the CPU 12 then directs one of the DMAs, such as 120, to read the status register 240 for device 24. Meanwhile, the CPU 12 processes the local interrupt from device 20 are 46, by reading the status register 202, performing the necessary tasks need~~ed~~ to handle the interrupt, and then clears~~inge~~ the status register 202."

On page 10, line 11, please replace the paragraph with:

"Thus, although there has been described to this point a particular embodiment for a method and apparatus for ~~multi-epoch codebook learning~~ improved interrupt efficiency across expansion busses, it is not intended that such specific references be considered as limitations upon the scope of this invention except in-so-far as set forth in the following claims."